



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application: **Erickson et al.**

Serial No.: **10/668,875**

Filed: **September 23, 2003**

For: **High Performance Voltage  
Control Diffusion Resistor**

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Group Art Unit: **2832**

Examiner: **Easthom, Karl D.**

Attorney Docket No.: **03-1091**

**Certificate of Mailing Under 37 C.F.R. § 1.8(a)**

I hereby certify this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on August 8, 2005.

By: \_\_\_\_\_

*Stephanie Fay*  
Stephanie Fay

**PRELIMINARY AMENDMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

No fees are believed to be necessary. If, however, any fees are required, I authorize the Commissioner to charge these fees to LSI Logic Corporation Deposit Account No. 12-2252. No extension of time is believed to be necessary. If, however, an extension of time is necessary, I authorize the Commissioner to charge the necessary extension fees to LSI Logic Corporation Deposit Account No. 12-2252.

Prior to continued examination of this application, please amend the above-identified application as follows:

**Listing of Claims** begins on page 2 of this paper.

**Remarks** begin on page 6 of this paper.